



晶采光電科技股份有限公司
AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-320240LATNQW-30H-B
APPROVED BY	
DATE	

- Approved For Specifications
 Approved For Specifications & Sample

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APPROVED BY	CHECKED BY	ORGANIZED BY

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2018/06/22	--	New Release	Mark

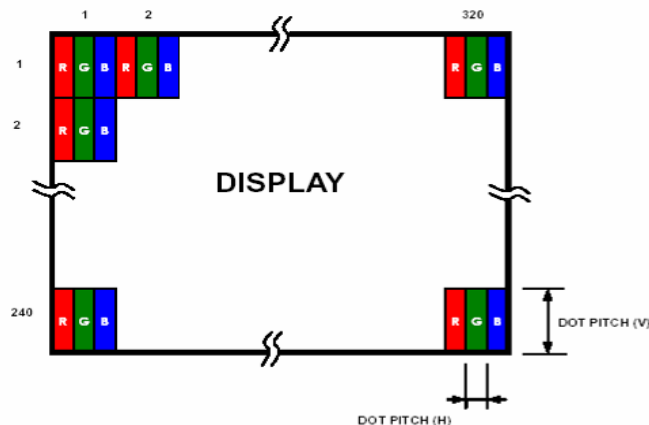
1 General Description and Features

3.5 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 3.5" TFT-LCD panel, a driver circuit and backlight unit.

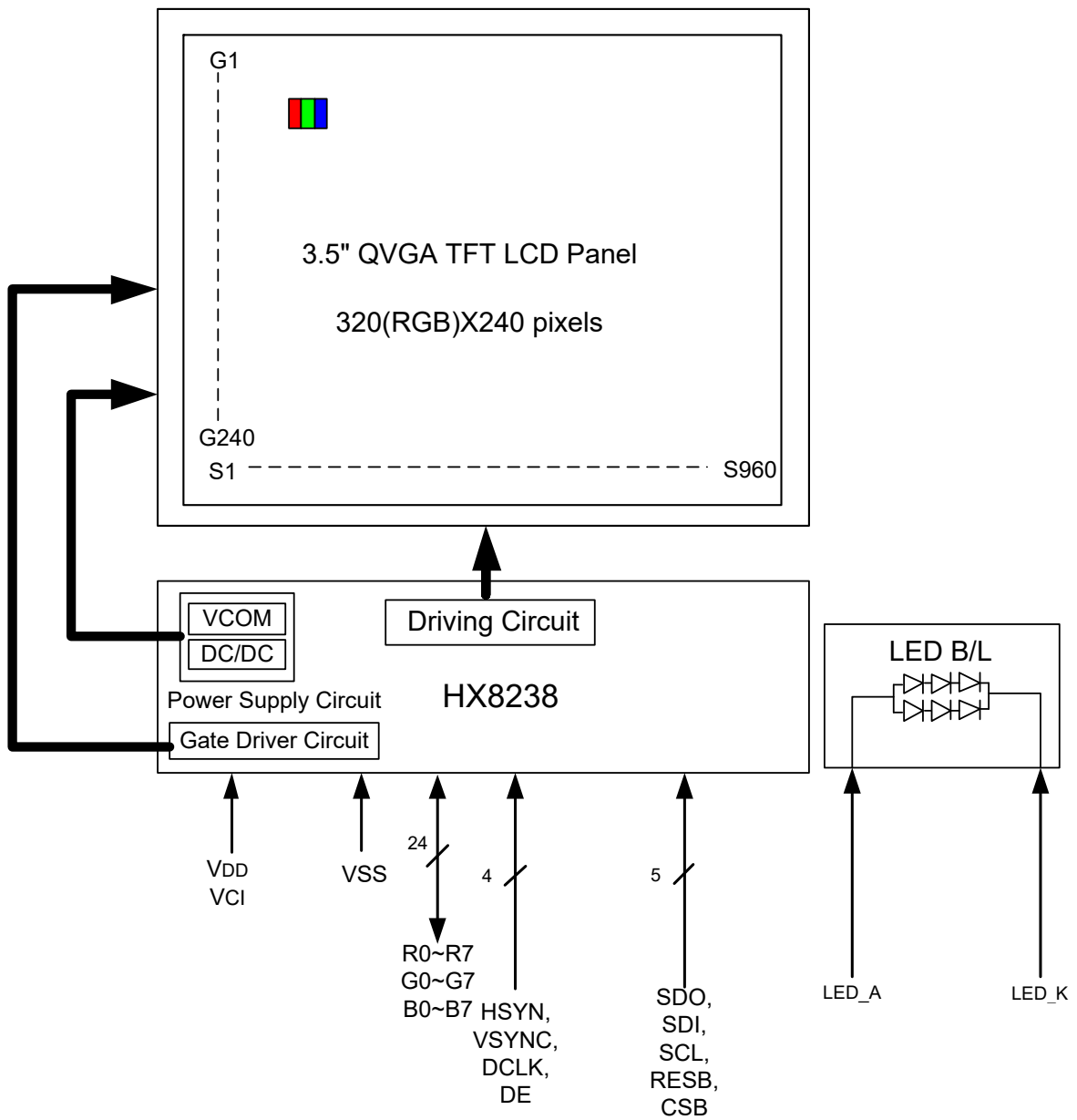
- 1.1 Construction: 3.5" a-Si color TFT-LCD, White LED Backlight and PCB.
- 1.2 Resolution (pixel): 320(R.G.B) X240.
- 1.3 Number of the Colors:16M colors (R, G, B 8 bit digital each).
- 1.4 LCD type: Transmissive Color TFT LCD (normally White).
- 1.5 View Angle: 12 o'clock (Gray Inversion)
- 1.6 24-Bit RGB Interface.
- 1.7 Interface: 50 pin.
- 1.8 Support DE Mode and SYNC Mode select by initial code.
- 1.9 Power Supply Voltage: 3.3V single power input. Built-in power supply circuit.
- 1.10 LED Type Backlight.
- 1.11 LCD Driver IC : HX8238-D00BPD400

2 Physical specifications

Item	Specifications	unit
Display Resolution	320(W) x 240(H)	dot
Active area	70.08 x 52.56	mm
Screen size	3.5(Diagonal)	inch
Dot pitch	0.073 (W) x 0.219 (H)	mm
Color configuration	R.G.B – stripe	
Overall Dimension	77.8(W) x 64.5(H) x 3.2(T)	mm
Input interface	digital 24-bits RGB	
Surface Treatment	Anti - glare(AG)	
Backlight unit	White LED	
Display Mode	Normally White/Transmissive	



3 Functional Block Diagram



4 Electrical Specifications

TFT LCD Panel FPC Descriptions

Pin	Name	I/O	Description
1	VSS	P	System ground of the IC
2	N.C.		Not Connected
3	SDO.	O	Serial data output in serial mode.
4	RESB	I	System reset pin; internal pull high.
5	CSB	I	Chip select pin of serial interface; internal pull high.
6	SCK	I	Clock pin of serial interface; internal pull high.
7	SDI	I	Serial data input in serial mode; internal pull high.
8	B0	I	Graphic data input pin
9	B1	I	Graphic data input pin
10	B2	I	Graphic data input pin
11	B3	I	Graphic data input pin
12	B4	I	Graphic data input pin
13	B5	I	Graphic data input pin
14	B6	I	Graphic data input pin
15	B7	I	Graphic data input pin
16	G0	I	Graphic data input pin
17	G1	I	Graphic data input pin
18	G2	I	Graphic data input pin
19	G3	I	Graphic data input pin
20	G4	I	Graphic data input pin
21	G5	I	Graphic data input pin
22	G6	I	Graphic data input pin
23	G7	I	Graphic data input pin
24	R0	I	Graphic data input pin
25	R1	I	Graphic data input pin
26	R2	I	Graphic data input pin
27	R3	I	Graphic data input pin
28	R4	I	Graphic data input pin
29	R5	I	Graphic data input pin
30	R6	I	Graphic data input pin
31	R7	I	Graphic data input pin
32	DEN	I	Display ENABLE for Data Transaction Timing in Parallel RGB (24 bit) Interface (DE Mode).
33	HSYNC	I	Horizontal SYNC for Data Transaction Timing in Parallel RGB (24 bit) Interface (SYNC Mode).
34	VSYNC	I	Vertical SYNC for Data Transaction Timing in Parallel RGB (24 bit) Interface (SYNC Mode).
35	DOTCLK	I	DOT CLOCK line; Latching data at the rising edge.
36	N.C.		Not Connected
37	N.C.		Not Connected
38	N.C.		Not Connected
39	N.C.		Not Connected
40	N.C.		Not Connected
41	N.C.		Not Connected
42	N.C.		Not Connected
43	N.C.		Not Connected
44	N.C.		Not Connected
45	VCI	P	Power supply for analog circuits, connected to 3,3V power
46	VDD	P	Voltage input pin for I/O logic ,connected to 3,3V power
47	N.C.		Not Connected
48	N.C.		Not Connected
49	K	P	Backlight LED's cathode
50	A	P	Backlight LED's anode

NOTE:

1. Gate scan from G0 to G239.
2. First RGB data at S0-S2.
3. Data "0" to maximum pixel voltage for normally white panel.
4. Red-Green-Blue color mapping.
5. HSYNC/VSYNC Mode for Parallel RGB Data Transaction.(default)
6. FPC length = 27.5mm \pm 0.5mm.

5 Basic Display Color and Gray Scale

COLOR		INPUT DATA																							
		R DATA								G DATA								B DATA							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB				LSB				MSB				LSB				MSB				LSB			
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(1)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

6 Absolute Maximum Ratings

If the operating condition exceeds the following absolute maximum ratings, the TFT LCD module may be damaged permanently.

6.1 Environmental Absolute max. ratings

Item	OPERATING		STORAGE		Remark
	MIN	MAX	MIN	MAX	
Temperature	-20	70	-30	80	Note2,3,4,5,6,7
Humidity	Note1		Note1		
Corrosive Gas	Not Acceptable		Not Acceptable		

Note 1 : $T_a \leq 40^\circ\text{C}$: 85% RH max

$T_a > 40^\circ\text{C}$: Absolute humidity must be lower than the humidity of 85%RH at 40°C

Note 2 : For storage condition T_a at $-30^\circ\text{C} < 48\text{h}$, at $80^\circ\text{C} < 100\text{h}$

For operating condition T_a at $-20^\circ\text{C} < 100\text{h}$

Note 3 : Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4 : The response time will be slower at low temperature.

Note 5 : Only operation is guaranteed at operating temperature. Contrast , response time, another display quality are evaluated at $+25^\circ\text{C}$

Note 6 : This is panel surface temperature, not ambient temperature.

Note 7 : When LCM be operated over than 40°C , the life time of the LED back-light will be reduced.

6.2 Electrical Absolute max. ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VDD	VSS=0	-0.3	4.0	V	
Power Supply for Analog Circuits	VCI	VSS=0	-0.3	4.0	V	
Input voltage	V_{in}		-0.3	VDD+0.3	V	Note 1

Note1:Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5

7 Electrical Characteristics

7.1 DC Electrical characteristic of the LCD

Typical operating conditions (VSS=0V)

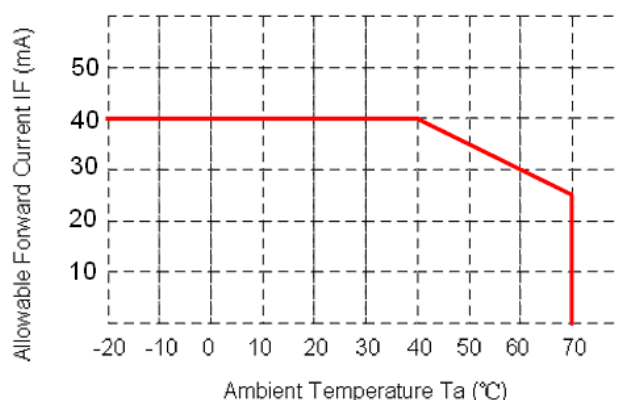
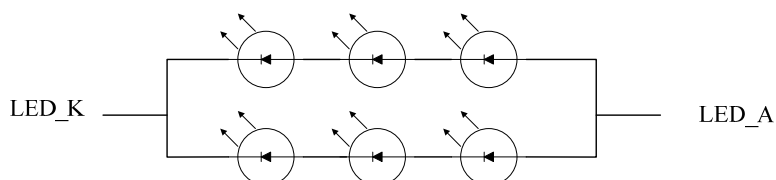
Item	Symbol	Min.	Typ.	Max.	Unit	Remark	
Power supply	VDD	3.0	3.3	3.6	V		
Power Supply for Analog Circuits	VCI	2.5	-	3.6	V		
Input Voltage for logic	H Level	V_{IH}	0.7 VDD	-	VDD	V	Note 1
	L Level	V_{IL}	0	-	0.3 VDD	V	
Power Supply current	IDD		15	--	mA	Note 2	

Note1: Hsync, Vsync, DEN, DCLK, R0~R7, G0~G7, B0~B7

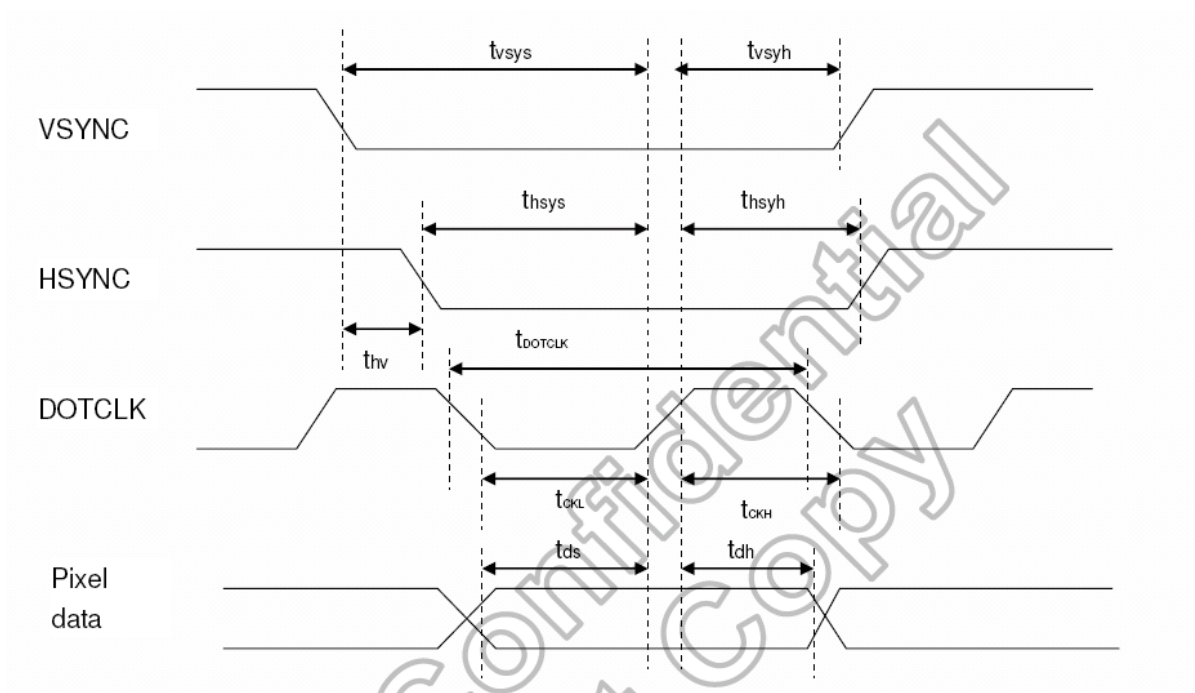
Note2: $f_v = 60\text{Hz}$, $T_a = 25^\circ\text{C}$, Display pattern : All Black

7.2 Electrical characteristic of LED Back-light

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
LED voltage	V_{AK}	--	9.6	--	V	$I_{LED} = 40\text{mA}$, $T_a = 25^\circ\text{C}$
LED forward current	I_{LED}	--	40	-	mA	$T_a = 25^\circ\text{C}$
	I_{LED}	--	20	-	mA	$T_a = 60^\circ\text{C}$
LED life time			30K		Hr	$T_a = 25^\circ\text{C}$ $I_{LED} = 26\text{mA}$



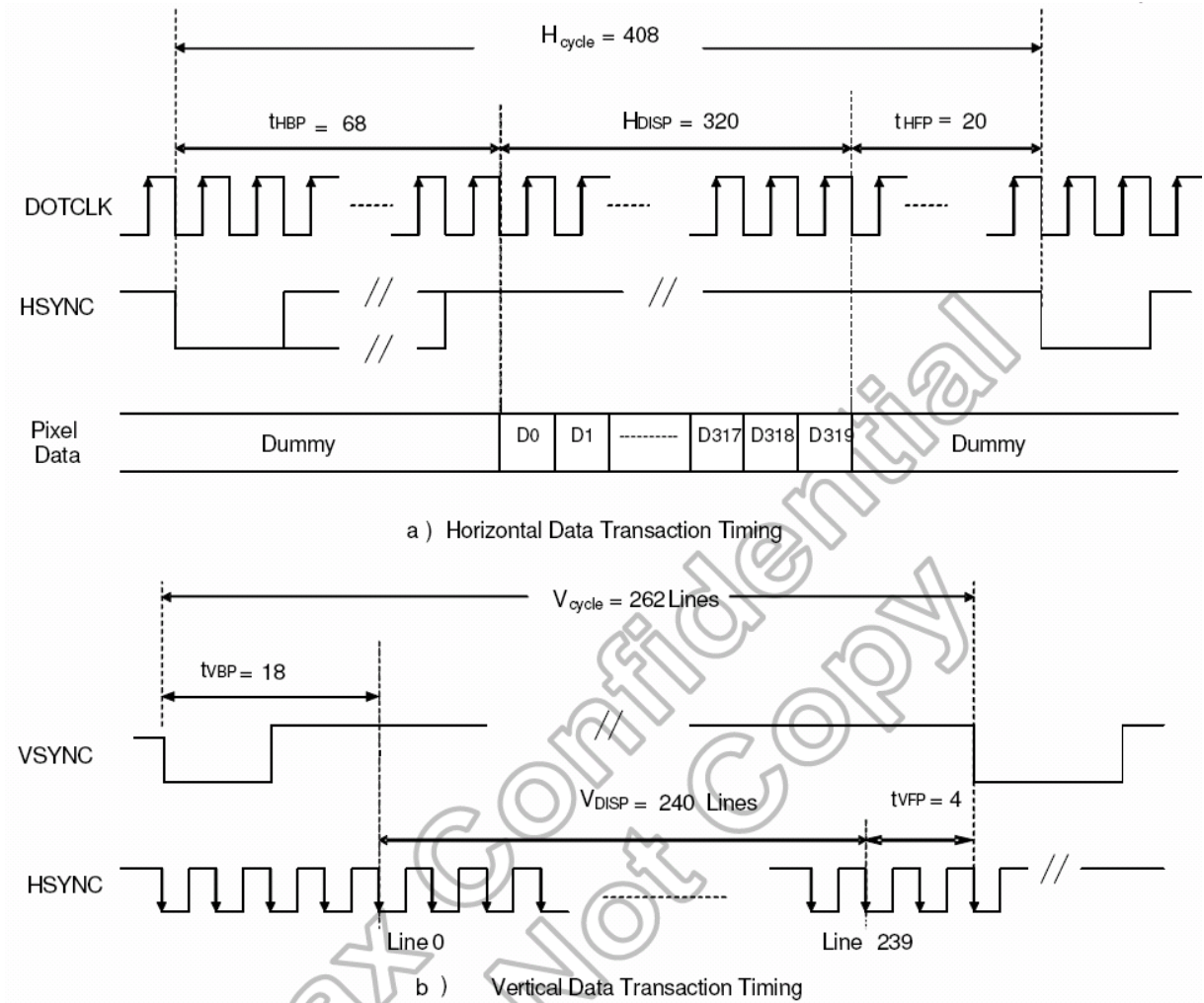
8 AC Timing characteristic of the LCD



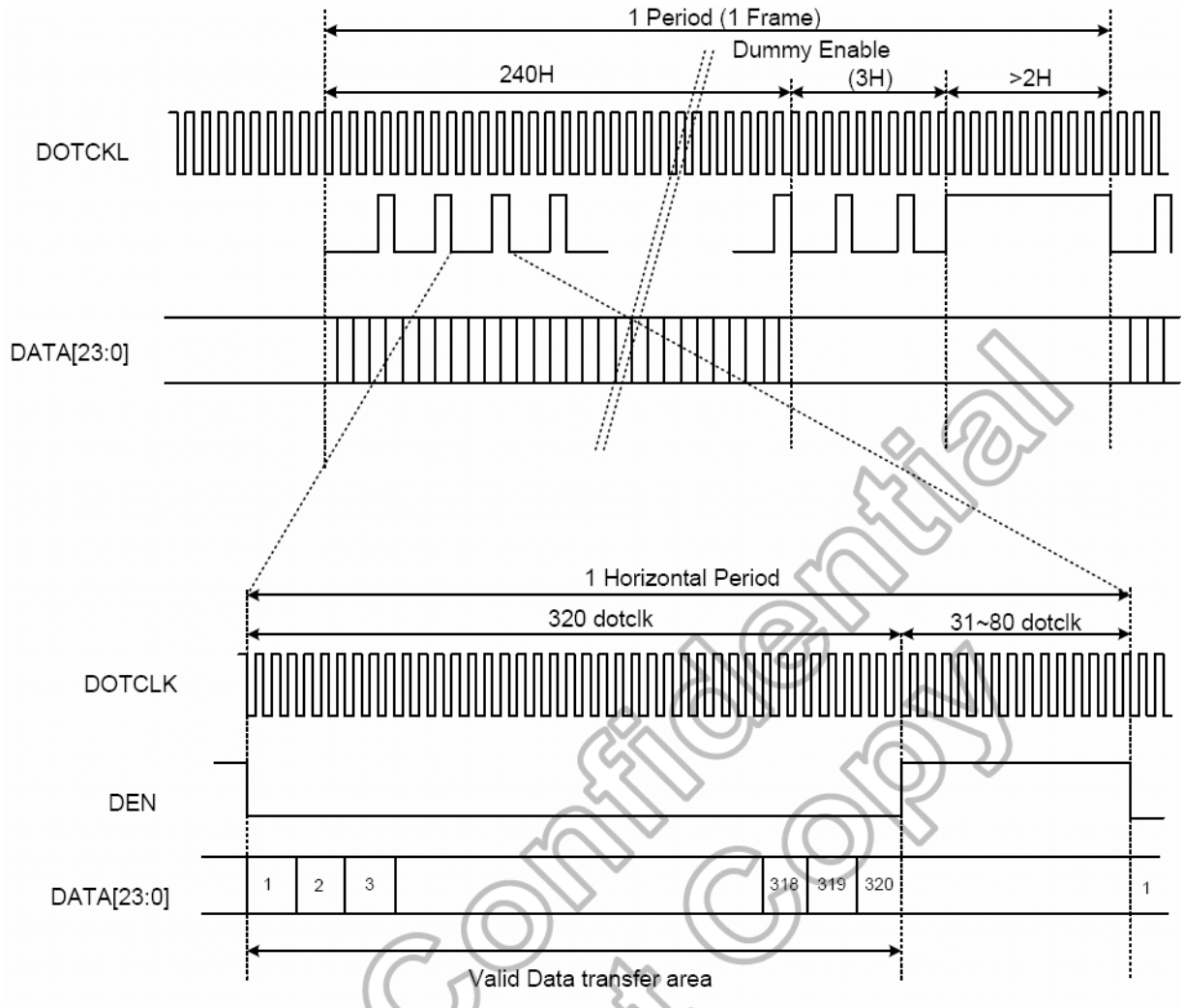
Characteristics	Symbol	Min		Typ		Max		Unit
		24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	
DOTCLK Frequency	fDOTCLK	-	-	6.5	19.5	10	30	MHz
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	-	-	ns
Vertical Sync Setup Time	tvsys	20	10	-	-	-	-	ns
Vertical Sync Hold Time	tvsyh	20	10	-	-	-	-	ns
Horizontal Sync Setup Time	thsys	20	10	-	-	-	-	ns
Horizontal Sync Hold Time	thsyh	20	10	-	-	-	-	ns
Phase difference of Sync Signal Falling Edge	thv	1		-		240		tDOTCLK
DOTCLK Low Period	tCKL	50	15	-	-	-	-	ns
DOTCLK High Period	tCKH	50	15	-	-	-	-	ns
Data Setup Time	tds	12	10	-	-	-	-	ns
Data hold Time	tdh	12	10	-	-	-	-	ns
Reset pulse width	tRES	10		-		-		us

Note: External clock source must be provided to DOTCLK pin of HX8238-A. The driver will not operate if absent of the clocking signal.

Pixel Timing Table



(a) Data Transaction Timing in Parallel RGB (24 bit) Interface (SYNC Mode)



b) Data Transaction Timing in Parallel RGB (24 bit) Interface (DE Mode)

Characteristics	Symbol	Min		Typ		Max		Unit
		24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	
DOTCLK Frequency	fDOTCLK	-	-	6.5	19.5	10	30	MHz
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	-	-	ns
Horizontal Frequency (Line)	fH	-	-	14.9		22.35		KHz
Vertical Frequency (Refresh)	fV	-	-	60		90		Hz
Horizontal Back Porch	tHBP	-	-	68	204	-	-	tDOTCLK
Horizontal Front Porch	tHFP	-	-	20	60	-	-	tDOTCLK
Horizontal Data Start Point	tHBP	-	-	68	204	-	-	tDOTCLK
Horizontal Blanking Period	tHBP + tHFP	-	-	88	264	-	-	tDOTCLK
Horizontal Display Area	HDISP	-	-	320	960	-	-	tDOTCLK
Horizontal Cycle	Hcycle	-	-	408	1224	450	1350	tDOTCLK
Vertical Back Porch	tVBP	-	-	18		-		Lines
Vertical Front Porch	tVFP	-	-	4		-		Lines
Vertical Data Start Point	tVBP	-	-	18		-		Lines
Vertical Blanking Period	tVBP + tVFP	-	-	22		-		Lines
Vertical Display Area	NTSC	-	-	240		-	-	Lines
	PAL			280(PALM=0)				
				288(PALM=1)				
Vertical Cycle	NTSC	-	-	262		350	-	Lines
	PAL			313				

Data Transaction Timing in Normal Operating Mode

9 Optical specification

9.1 Optical characteristic of the LCD

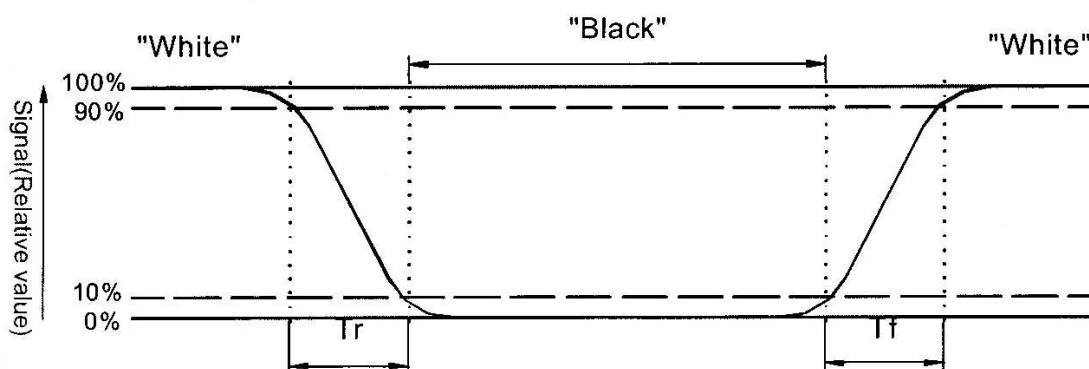
Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Response Time	T_r+T_f	$\Theta=0^\circ$		50	80	ms	Note 1,2,3,5
Contrast ratio	CR	At optimized viewing angle	-	300	-		Note 1,2,4,5
Viewing Angle	Top	$CR \geq 10$	-	70	-	deg.	Note1,2, 5,6
	Bottom		-	50	-		
	Left		-	70	-		
	Right		-	70	-		
Brightness	Y_L	$I_{LED}=40mA$ $,25^\circ C$	-	600	-	cd/m^2	Note 7
White chromaticity	XW		0.25	-	0.35		
	YW		0.26	-	0.36		

Note 1: Note 1: Ambient temperature= $25^\circ C$, and lamp current $I_{LED}=20mA$. To be measured in the dark room.

Note 2: To be measured on the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation.

Note 3. Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 4. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

Contrast ratio (CR) = _____

Note 5: White $V_i = V_{i50} + 1.5V$

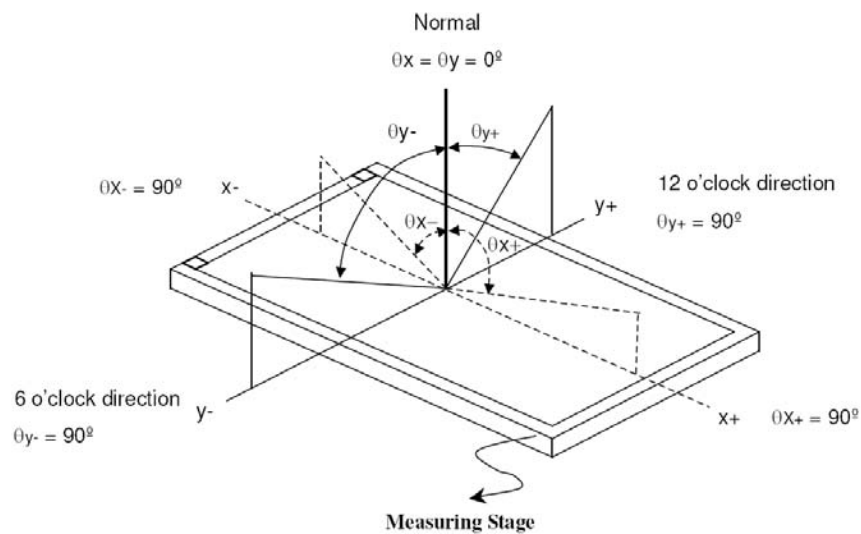
Black $V_i = V_{i50} + 2.0V$

“±” means that the analog input signal swings in phase with V_{COM} signal.

“ $\frac{-}{+}$ ” means that the analog input signal swings out of phase with V_{COM} signal.

V_{i50} : The analog input voltage when transmission is 50%. The 100% Transmission is defined as the transmission of LCD panel when all the Input terminals of module are electrically opened.

Note 6. Definition of viewing angle, Refer to figure as below.



Note 7. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

10 RELIABILITY

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=96 hrs	
Low Temperature Operation	-20±3°C , t=96 hrs	
High Temperature Storage	80±3°C , t=96 hrs	1,2
Low Temperature Storage	-30±3°C , t=96 hrs	1,2
Humidity Test	40°C , Humidity 90%, 96 hrs	1,2
Thermal Shock Test	-30°C ~ 25°C ~ 80°C 30 min. 5 min. 30 min. (1 cycle) Total 5 cycle	1,2
Vibration Test (Packing)	Sweep frequency : 10~55~10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions

(15-35°C , 45-65%RH).

Definitions of life end point :

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

11 USE PRECAUTIONS

11.1 Handling precautions

- 1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- 2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- 3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- 4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

11.2 Installing precautions

- 1) To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. $1M\Omega$ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- 2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- 3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- 4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

11.3 Storage precautions

- 1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- 2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- 3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

11.4 Operating precautions

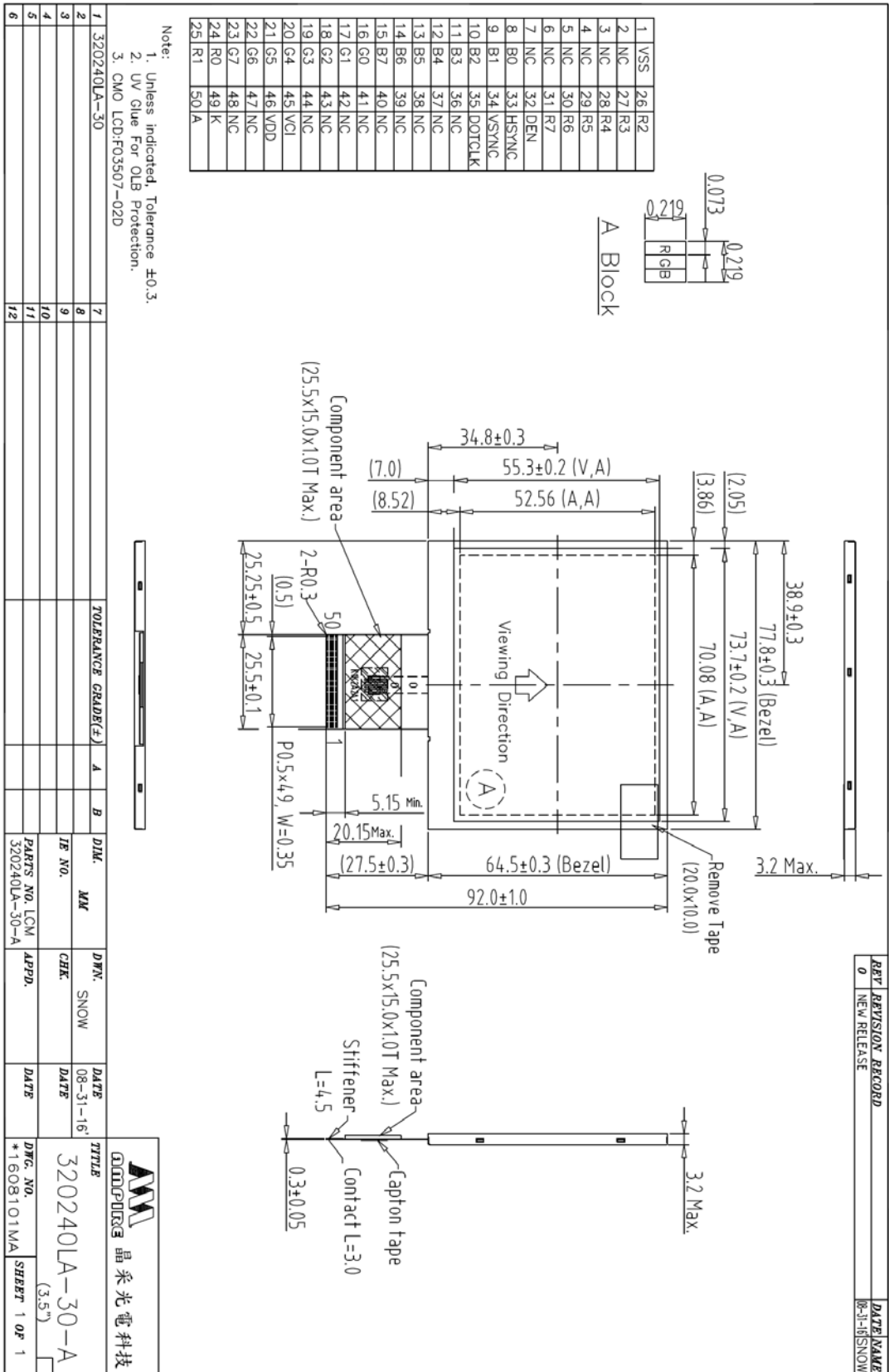
- 1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- 2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- 3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC drive voltage.
- 4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- 5) Make certain that each signal noise level is within the standard (L level: 0.2V_{dd} or less and H level: 0.8V_{dd} or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- 6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- 7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.

8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

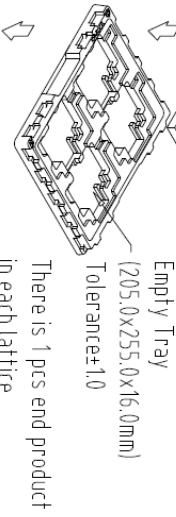
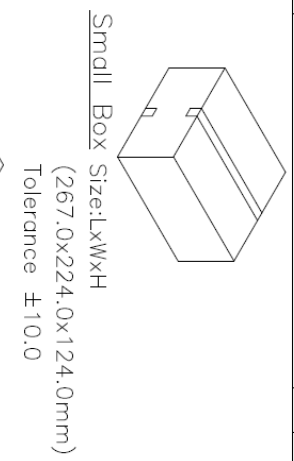
11.5 Other

- 1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- 2) Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.
- 3) AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.

12 Mechanical Dimensions

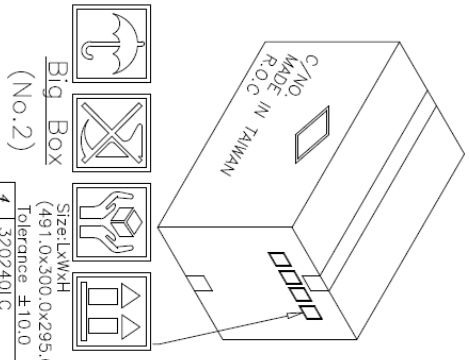
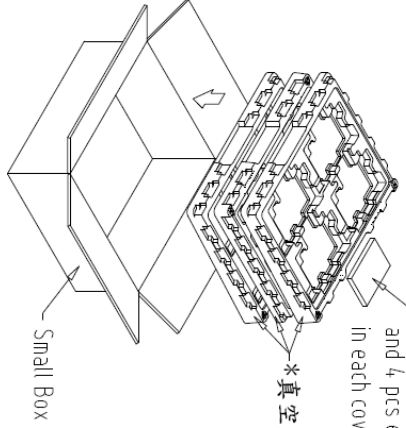
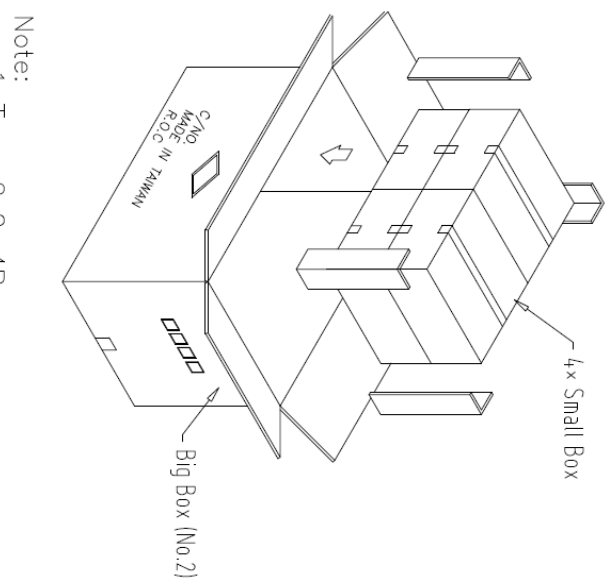


REV	REVISION RECORD	DATE NAME
0	NEW RELEASE	10-14-11 Henry



There is 1 pcs end product in each lattice, and 4 pcs end products in each cover.

*真空盒交錯堆疊



4	3202401C
3	3202401B
2	3202401A
1	3202401A

No. 適用品號

AMP 晶采光電科技

Note:
1 Tray=2x2=4Pcs.
2 Small Box=8xTray=32Pcs.(9 Tray)
3 Big Box=4xSmall Box=128Pcs.

1	交叉堆疊	7	TOLERANCE GRAD(E)	A	B	DM.	MM	DM.	DATE
2		8				IE NO.		Henry	10-14-11
3		9				CHEK.			
4		10				PARTS NO. BOX	APPD.		
5		11				320240L2			
6		12							

TITLE 320240L (3.5")

DMG. NO. *111041SA

SHEET 1 OF 1